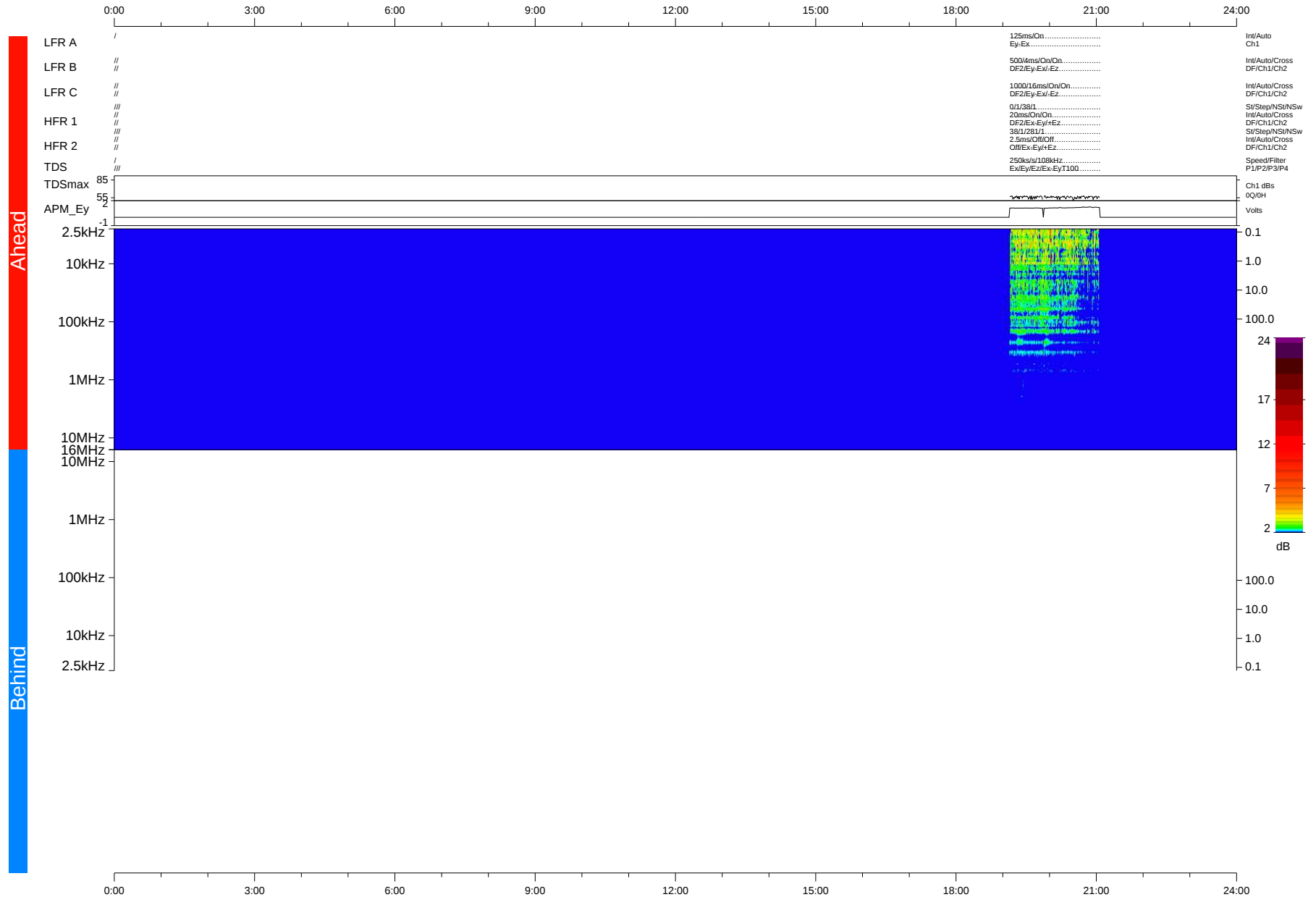


DPU up 145.6d, V412d26



DPU up 0.0d, Vd0
2016-02-26 19:07:04, TMLib V943 dynspec V210