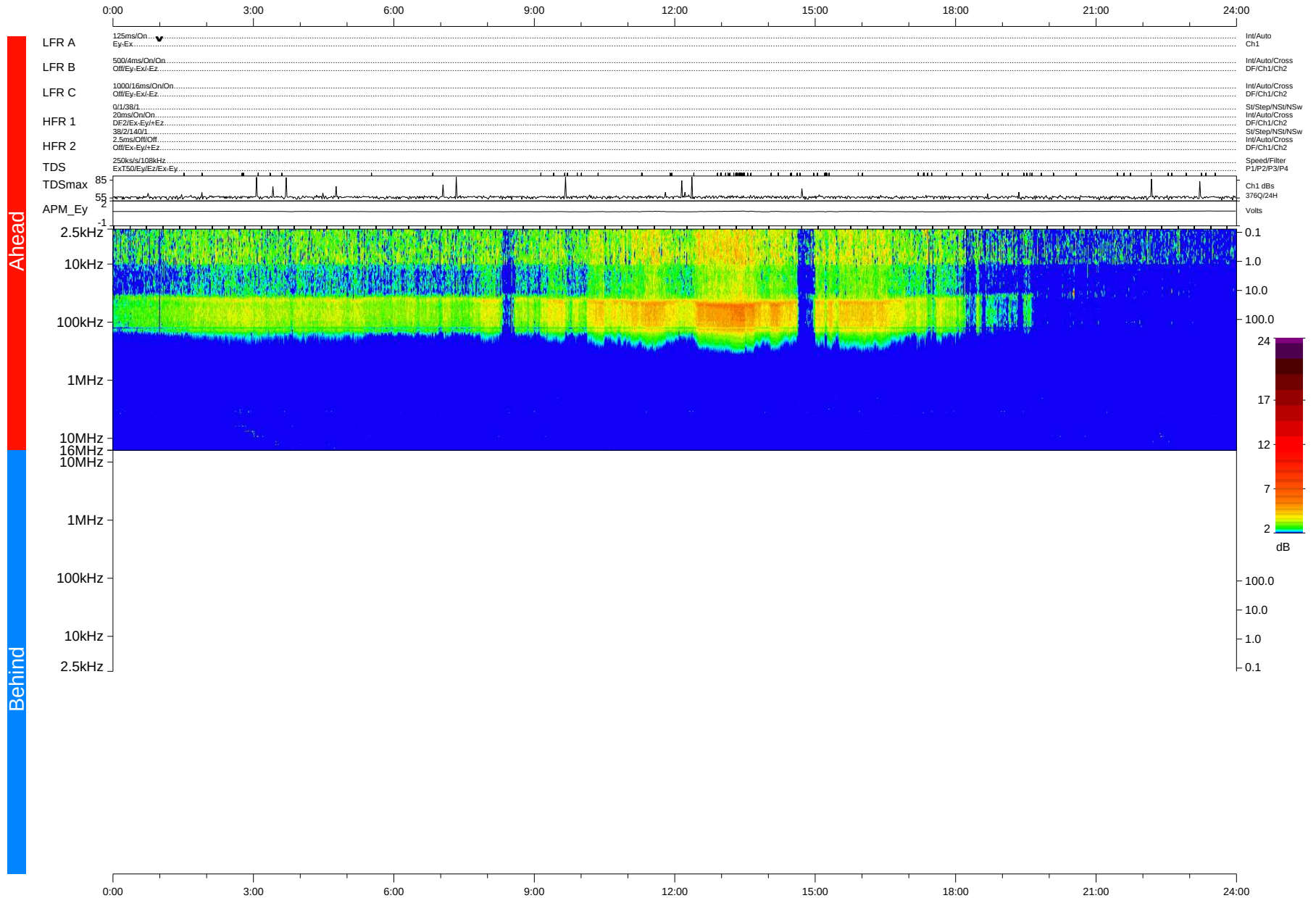


DPU up 1231.9d, V412d32



DPU up 0.0d, Vd0
2018-12-20 03:55:28, TMLib V943 dynspec V210