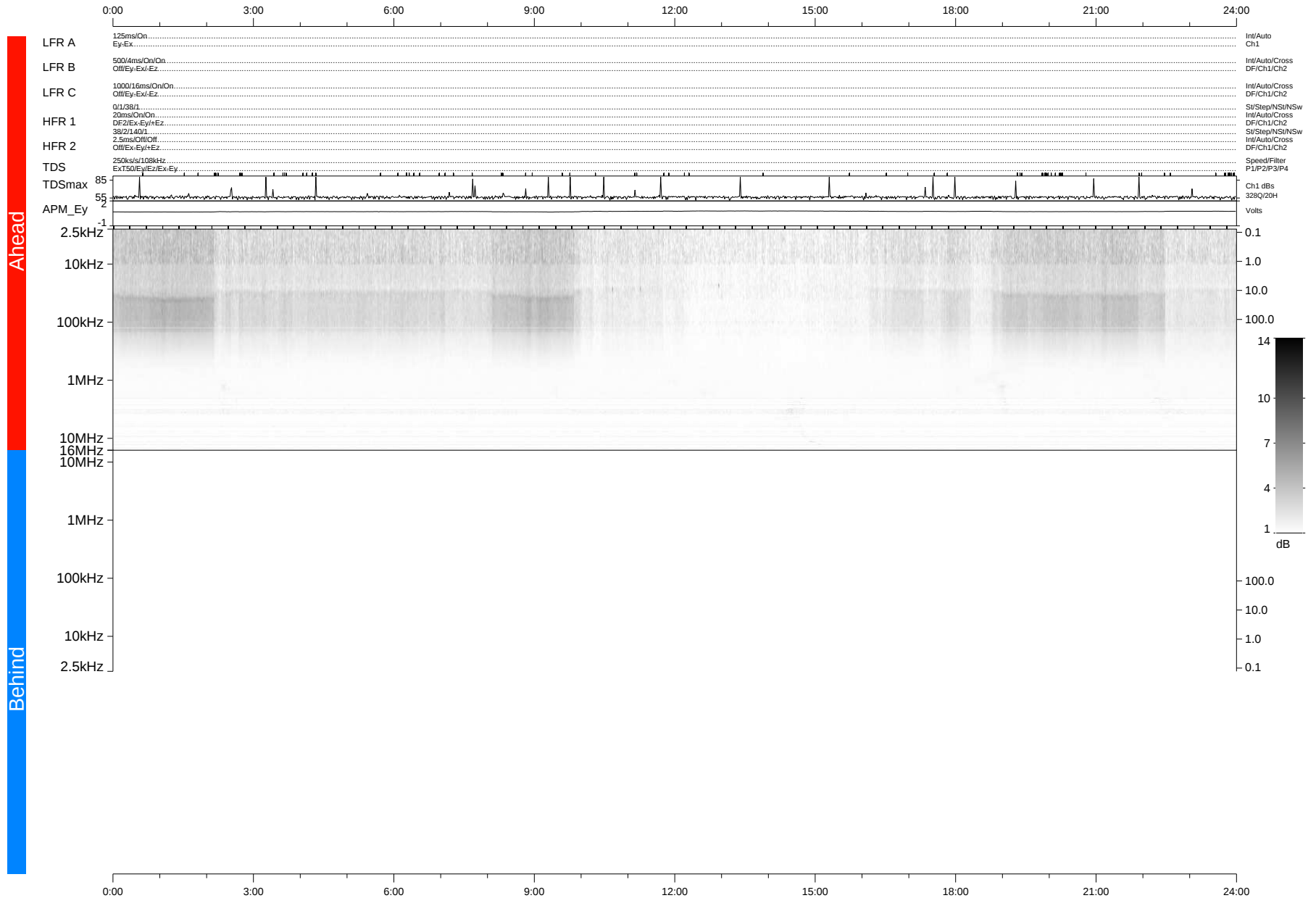


DPU up 1204.9d, V412d32



Behind source file = swaves_ ahead_ 2018_ 297_ 1_ 05.fin

DPU up 0.0d, Vd0
2018-11-23 05:56:01, TMLib V943 dynspec V210