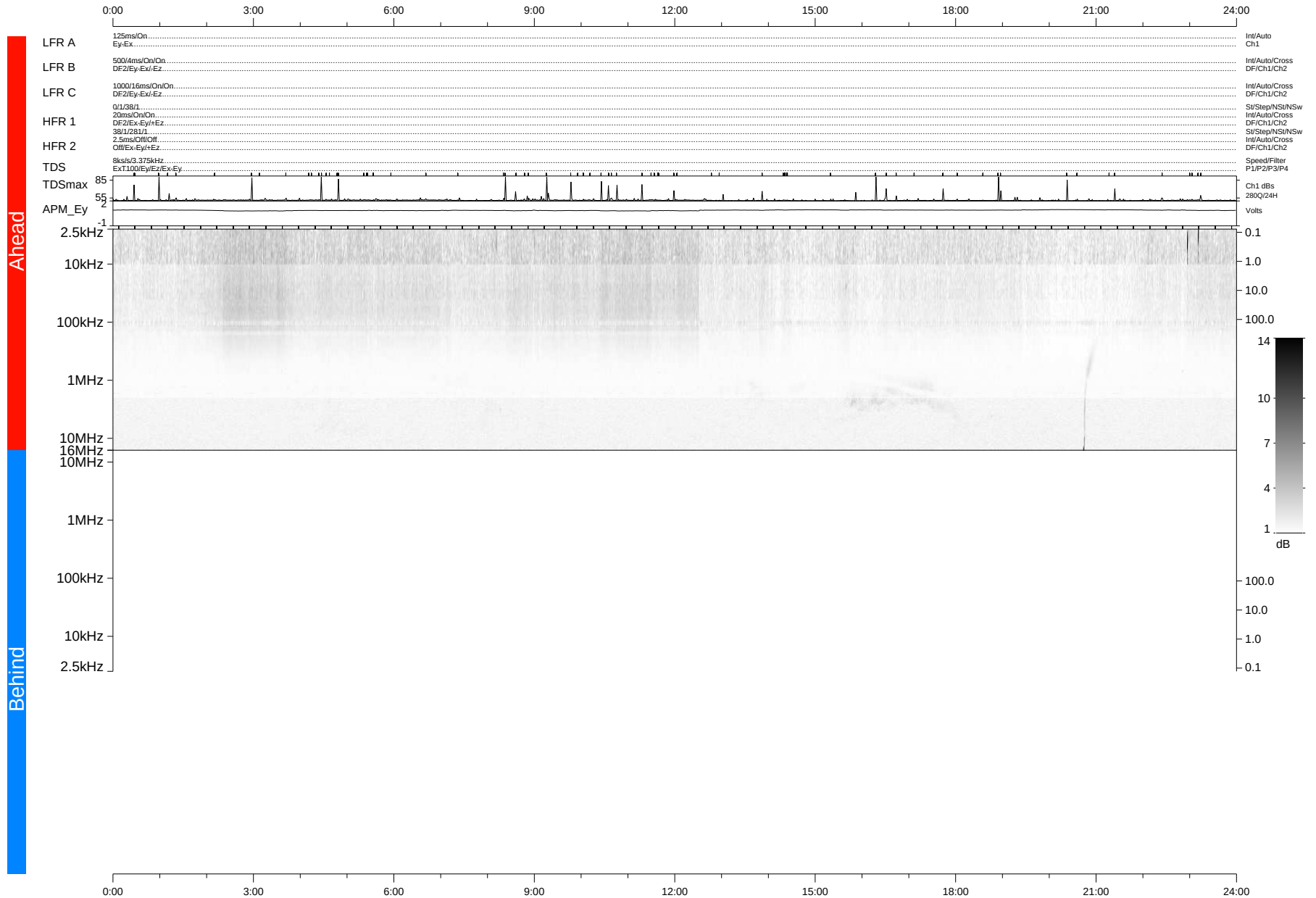


DPU up 942.9d, V412d28



DPU up 0.0d, Vd0
2018-02-08 04:54:53, TMLib V943 dynspec V210