

IMPACT Subsystem Milestones

SEP Cen - SEP Central Data Processing Unit

ITEM	EVENT	2003												2004									
		N	D	J	F	M	A	M	J	J	A	S	O	N	D	J	F	M	A	M	J	J	A
1	EM Logic Board Fabricated				3/19	▼																	
2	EM Analog/Post-reg Board Fabricated				3/28	▼																	
3	EM Solid State Detector Bias Supply Board				4/2	▼																	
4	B1 Flight Software Available				4/8	▼																	
5	EM Analog/Post-reg Board Assembled				4/11	◆	4/14																
6	EM Logic Board Assembled				4/14	▽	◆	5/13															
7	ETU LVPS Received				5/5	▽					◇	9/2											
8	EM Logic Board Tested				5/5	◆	5/8																
9	ETU LVPS Tested				5/14	▽					◇	9/3											
10	FM 1 SEP Central Common Enclosure Fabricated				5/22	▽					◇	10/23											
11	FM 2 SEP Central Common Enclosure Fabricated				5/22	▽					◇	10/23											
12	EM Analog/Post-reg Board Tested				5/28	▽	◆	6/24															
13	EM Solid State Detector Bias Supply Board Tested				5/28	▽					◇	8/18											
14	EM Boards Available for EM I & T				5/29	◆	6/9																
15	FM 1 SEP Central Common Electronics Enclosure				5/30	▽						◇	10/30										

▽ On Time

◇ Possibly Early

◇ Possibly Late

▼ Completed On Time

◆ Completed Early

◆ Completed Late

Mission Critical MS

IMPACT Critical MS

Suite MS

