IMPACT Subsystem Milestones SEP Cen - SEP Central Data Processing Unit

Page 1 of 3 Status as of:7/31/03

rayeı	010	Status as 01.7/31/0															_								
				2003										2004											
ITEM	EVENT	N	D	J	F	М	I A	М	J	J	Α	s	0	N	D	J	F	М	Α	М	J	J	Α	s (2
1	EM Logic Board Fabricated			;	3/19		4_																		
2	EM Analog/Post-reg Board Fabricated				3/2	8																			
3	EM Solid State Detector Bias Supply Board				4	/2																			
4	B1 Flight Software Available				,	 4/8 																			
5	EM Analog/Post-reg Board Assembled				4	 /11 		4/1	 4 																
6	EM Logic Board Assembled				4	 1/14 	1		5/	13															
7	ETU LVPS Received						5/5	\bigvee			-	\Diamond	9/2												
8	EM Logic Board Tested						5/5		5/8																
9	ETU LVPS Tested						5/14				-	\supset	9/3												
10	FM 1 SEP Central Common Enclosure Fabricated						5/22	2	7				$+\langle$	> 1	0/23										
11	FM 2 SEP Central Common Enclosure Fabricated						5/22	2	7				-	11	0/23										
12	EM Analog/Post-reg Board Tested						5/2	8		6	/24														
13	EM Solid State Detector Bias Supply Board Tested						5/2	8	$\overline{\mathcal{L}}$			8/	18												
14	EM Boards Available for EM I & T						5/2	9 \		6/9															
15	FM 1 SEP Central Common Electronics Enclosure						5/3	30	7				-	>	10/30)									
	√ On Time	V	Co	om	ple	tec	d Or	n Ti	me)								M	liss	ion	Cri	itica	al M	s	
	Possibly Early	•	Co	om	ple	te	d Ea	arly									IMPACT Critical MS								
	Possibly Late		Co	om	ple	te	d La	ite										S	uite	e M	S				

IMPACT Subsystem Milestones SEP Cen - SEP Central Data Processing Unit

Page 2 of 3 Status as of:7/31/03

aye 2																			<u> </u>	5	,	<u> </u>		_	
				2003 2004																					
ITEM	EVENT	N	D	J	F	М	Α	М	J	J	I A	. ;	s o	N	D	J	F	М	Α	М	J	J	Α	s)
16	FM 2 SEP Central Common Electronics Encloure						5/3	0/	\checkmark				<	\nearrow	10/30										
17	B2 Flight Software Available								7/3	\/	7/7	'													
18	Flight Logic Boards Fabricated								7/2	7 8 <u>2</u> 1		8	/1 4 												
19	Flight Logic Boards Assembled										9/17	7	$\overline{\mathcal{K}}$	10) 0/20 										
20	Flight Analog/Post-reg Boards Fabricated										9/5 <	$\langle \rangle$	9	/23 											
21	Flight Solid State Detector Bias Supply Boards										9/	'29 		10	 /15 										
22	Flight Analog/Post-reg Board Assembled										9/16	3	∇	10/2											
23	FM 1 LVPS Received										1	0/1:	3 🗸	7	+	12	2/24								
24	Flight Logic Boards Tested											10	/28	$\overline{\mathcal{X}}$	\bigcirc 1	 2/1 									
25	Flight Analog/Post-reg Boards Tested												11/6 <		7 1	1/25									
26	Flight Solid State Detector Bias Supply Boards Tested												1	2/9	K	12	 2/24 								
27	FM 1 LVPS Tested												12	2/11	V	\Diamond	1/13	3 							
28	FM 1 Ready for I & T												12	2/12	\bigvee		1/14	4							
29	FM 2 LVPS Received												1	2/17			1/1	19							
30	B3 Flight Software Available W/User Manual												1	2/18	3										
√ On Time					Completed On Time Mission Critical												al M	S							
	\	Co	om	ple	ted	Ea	arly	′							IMPACT Critical MS										
	Possibly Late		Co	om	ple	ted	La	te										S	uite	e M	S				

IMPACT Subsystem Milestones SEP Cen - SEP Central Data Processing Unit

Page 3 of 3 Status as of:7/31/03

- age (_													_									_		
									20	03	3								20	04	<u> </u>					
ITEM	EVENT	N	D	J	F	М	Α	М	J	J	Α	s	0	N E	J	F	М	Α	М	J	J	Α	s c			
31	FM 2 Ready for I & T													1/2	\bigvee	\bigcirc	2/3									
32	FM 2 LVPS Tested													1/2	V	\Diamond	2/3 									
33	Final Flight Software Available													1/7	\bigvee	<u>'</u>	2/	 /26 								
																								╛		
	✓ On Time	V	Co	omp	ple	ted	Or	ı Tiı	me								M	liss	ion	n Critical MS						
	Possibly Early	\	Co	omp	ple	ted	Ea	ırly									ΙΝ	/IP/	AC7	Γ Critical MS						
	Possibly Late Completed Late										Suite MS															