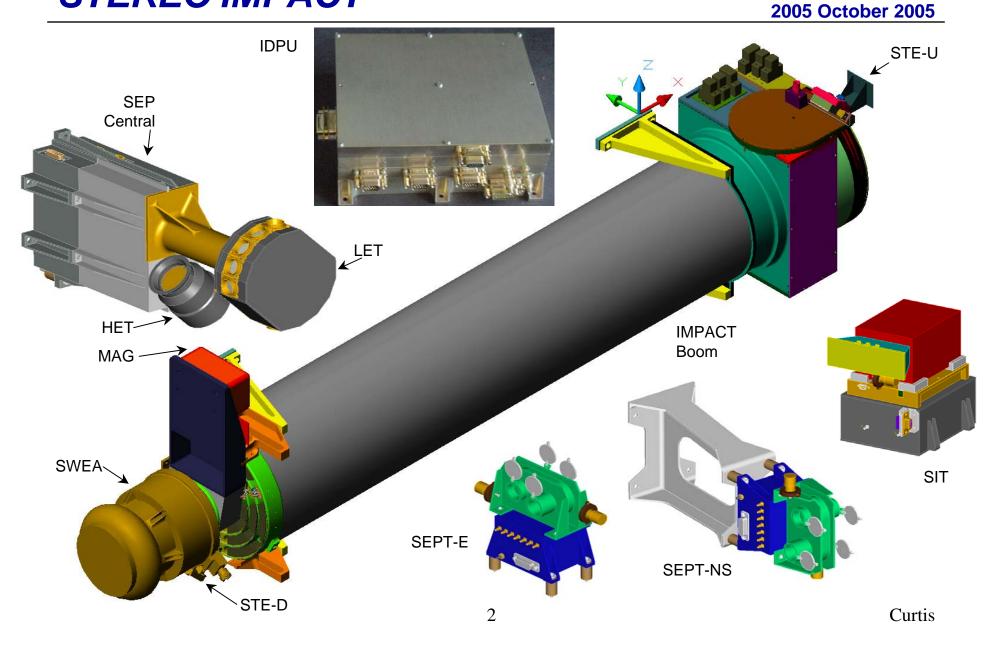
STEREO IMPACT



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STEREO IMPACT

Observatory Pre Environmental CPT Evaluation



IDPU CPT Evaluation

Subsystems Tested

Power supply (secondary voltages, primary current)
 In Limits, trend flat

Telemetry Output FormatOK

Command ProcessingOK

Housekeeping packet contents
 In Limits

EEPROM Write/dumpOK

Spacecraft, Instrument, and SWAVES status transfers

Memory TestOK

Watchdog Timer TestOK

Instrument Interfaces tested in instrument CPT subsections

Boom Evaluation

Subsystems Tested

Boom DeploymentNot Tested

Next (last) test EMC deployment

Boom Deployment Heater
 Not Tested

· Tested in thermal vac

- Harnessing OK

Tested as part of SWEA/STE Instrument tests

MAG Instrument Evaluation

Subsystems Tested:

MAG Housekeeping
 In Limits

MAG Data
 In Range

MAG In-Flight Cal (IFC) Mode
 In Limits

MAG HeaterNominal

NOTE: only standby current can be tested except in thermal vac

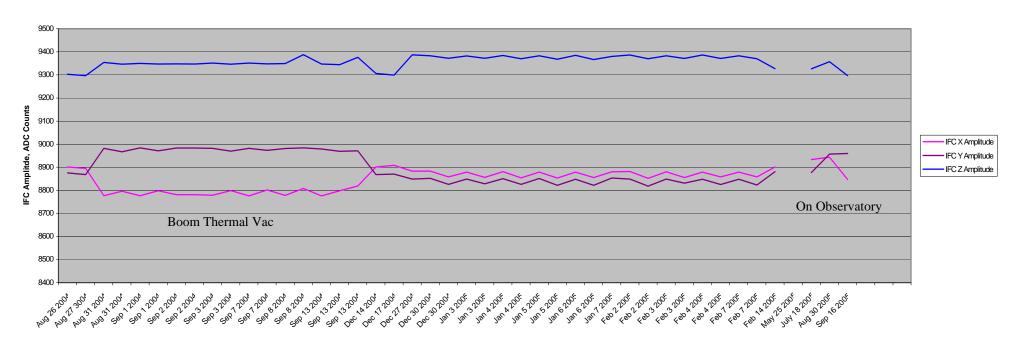
Performance Data:

MAG IFC trending, amplitude
 Nominal

MAG IFC trending, timing
 Nominal

Sample MAG Trending Data

MAG B In Flight Cal Fit



SWEA Instrument Evaluation

Subsystems Tested:

Power supply (secondary voltages, primary current)
 In Limits, trend flat

Analyzer High Voltage Supply
 In limits

MCP High Voltage Supply
 Not Tested

Detector end-to-end, background
 Not Tested

• MCP, analyzer also not tested with particles except in vacuum

Last tested in instrument thermal vac,

SWEA Housekeeping
 In Limits

Preamps, signal chainOK

SWEA Door
 Not Tested

Tested subsequently, OK

SWEA Operational HeaterOK

SWEA Survival Heater
 Not Tested

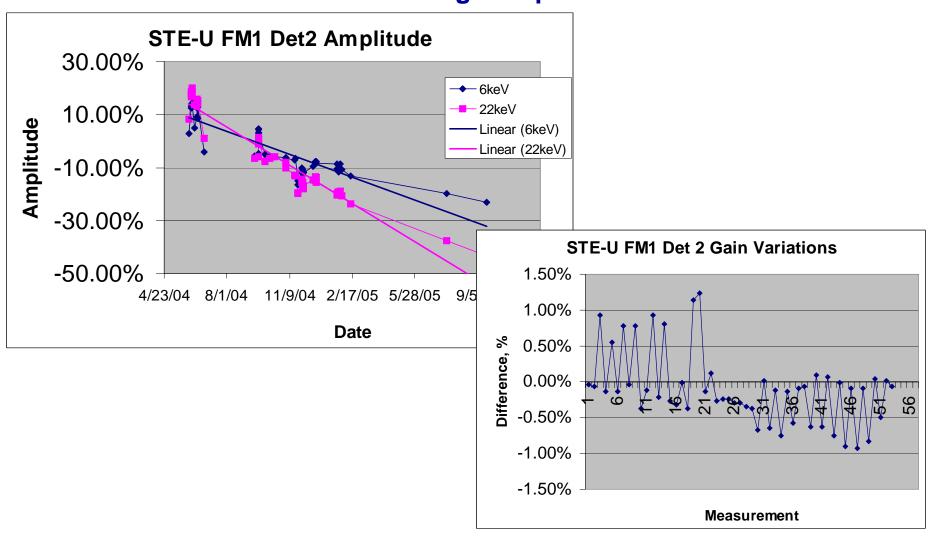
Tested in thermal vac

STE Instrument Evaluation

Subsystems Tested:

STE-U Housekeeping
 STE-D Housekeeping
 STE Noise Thresholds (Background)
 STE Test Pulser Response
 STE Door Source Calibrations
 STE Door Open/Close

STE Trending Samples



STEREO IMPACT

SEPT Instrument Evaluation

•	Subsy	ystems	tested:
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SEPT Housekeeping

Detector leakage currents

Temperature

Voltage rails 2.6D, 5.3D, 5.6A

Detector end-to-end, background

Analog electronics

Digital electronics

Commands

SEPT Science data

SEPT Doors

Limited life items

Tested subsequently

SEPT Operational Heater

SEPT Non-Operational Heater

In limits, trend with temperature

In limits

In limits, trend flat

OK, tested with ⁶⁰Co source

OK, tested with inflight test pulser

OK, tested with inflight test pulser

OK

OK

Not tested routinely

OK

Not tested except in thermal vac

Not tested except in thermal vac

SEPT Operational Heater control

FM1 SEP_Central CPT Evaluation

Subsystems Tested

-	LVPS (secondary voltages, primary current)	In limits, trend flat*
_	Post-Regulator for SIT (voltage only monitored)	In limits, trend flat*
_	Bias Supply (pos. & neg. voltage taps)	In limits, trend flat*
_	Housekeeping packet contents	In limits
_	SEP_Central boot message, checksums	OK
_	Sensor boot messages, checksums	OK
_	Sensor science packet App Id	OK
_	Sensor interfaces	OK
_	SEPT science packet format	OK

* Due to SEP sensor development at different institutions, SEP suite was mainly tested in piece-meal fashion before full integration on S/C. The housekeeping parameters show a predictable step in trending when loads change because of sensor configuration change.

OK

FM1 LET CPT Evaluation

Subsystems Tested:

Housekeeping (leakage currents & temperatures)
 In limits

MISC boot message, checksums
 OK

Internal Pulser

Quiet ModeOK

ADC ModeOK

Threshold ModeOK

Detector end-to-end
 Not routine CPT test

Tested during LET Source Test on S/C on 9/28/05

Preamps, signal chainOK

Operational Heater

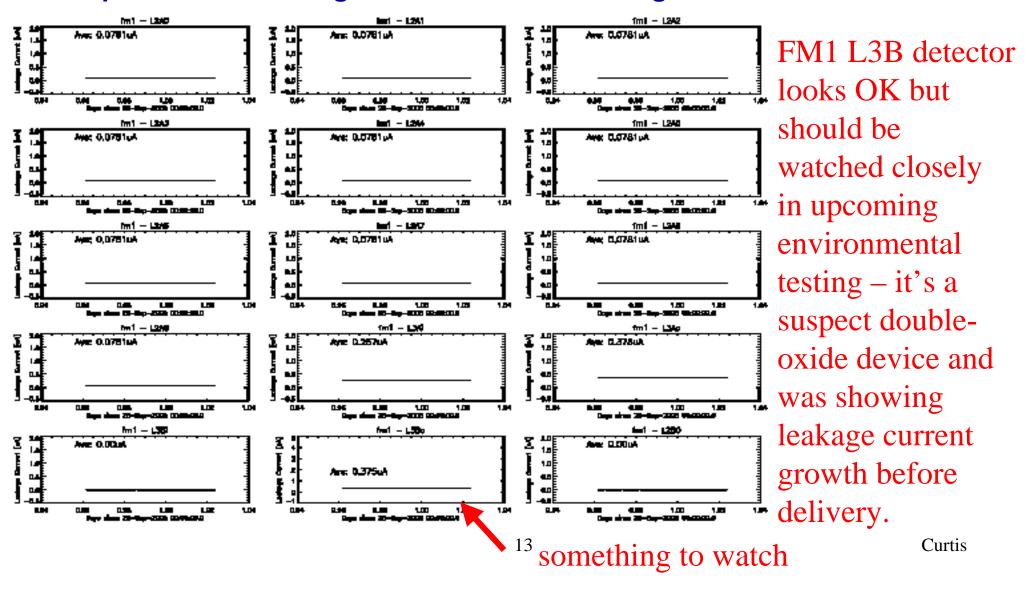
Performance Data:

Gains & Offsets trendingNominal

Leakage Currents vs. Temperature trending
 Nominal

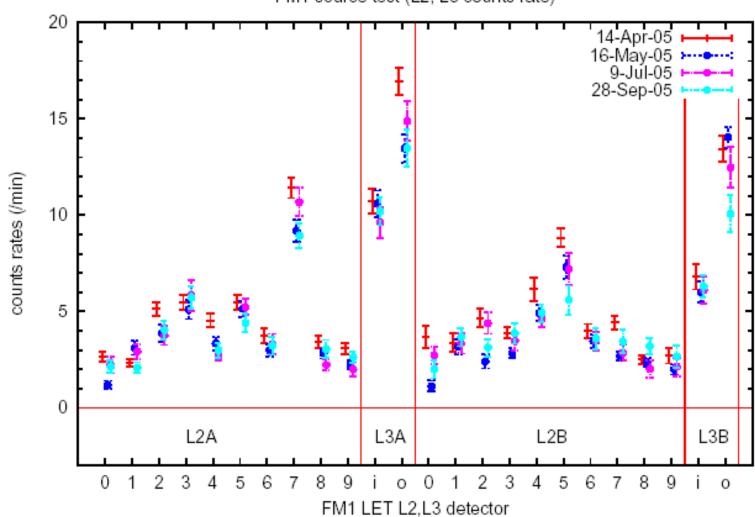
STEREO IMPACT

Sample FM1 LET leakage currents vs time during CPT



Sample FM 1 LET Trending Data (Source Tests)

FM1 source test (L2, L3 counts rate)



FM2 SEP_Central CPT Evaluation

Subsystems Tested

_	LVPS (secondary voltages, primary current)	In limits, trend flat*
_	Post-Regulator for SIT (voltage only monitored)	In limits, trend flat*
_	Bias Supply (pos. & neg. voltage taps)	In limits, trend flat*
_	Housekeeping packet contents	In limits
_	SEP_Central boot message, checksums	OK
_	Sensor boot messages, checksums	OK
_	Sensor science packet App Id	OK
_	Sensor interfaces	OK
_	SEPT science packet format	OK
_	SEPT Operational Heater control	OK

* Due to SEP sensor development at different institutions, SEP suite was mainly tested in piece-meal fashion before full integration on S/C. The housekeeping parameters show a predictable step in trending when loads change because of sensor configuration change.

STEREO IMPACT

FM2 LET CPT Evaluation

Subsystems Tested:

Housekeeping (leakage currents & temperatures)
 In limits

 L3B (suspect double-oxide device) may be showing growing leakage current

MISC boot message, checksums
 OK

Internal Pulser

Quiet Mode/ADC Mode/Threshold Mode
 OK/OK/OK

Detector end-to-end
 Not routine CPT test

Tested during LET Source Test on S/C on 9/19/05

Preamps, signal chainOK

Operational HeaterOK

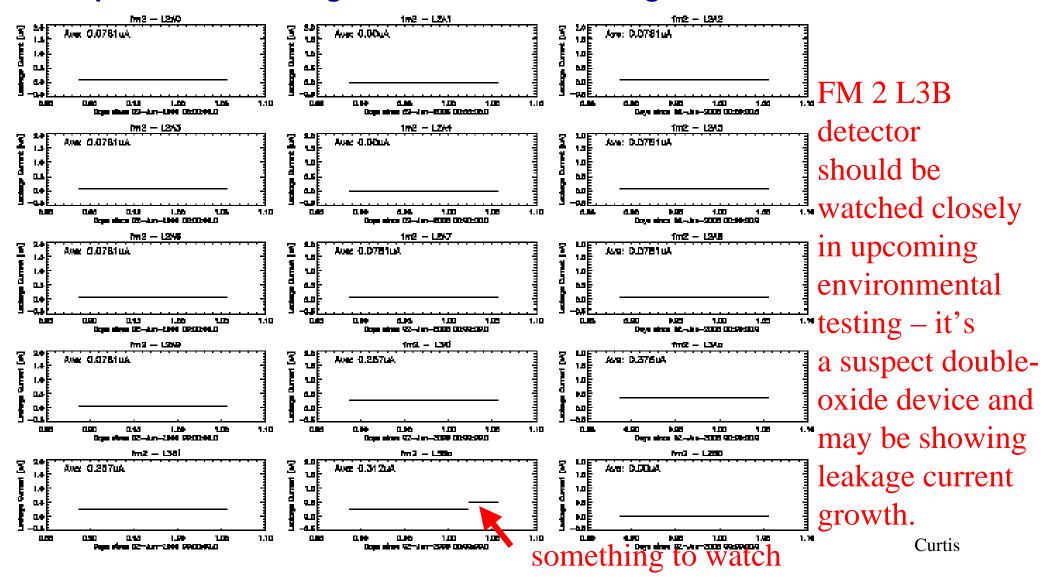
Performance Data:

Gains & Offsets trendingNominal

Leakage Currents vs. Temperature trending
 Nominal but see above

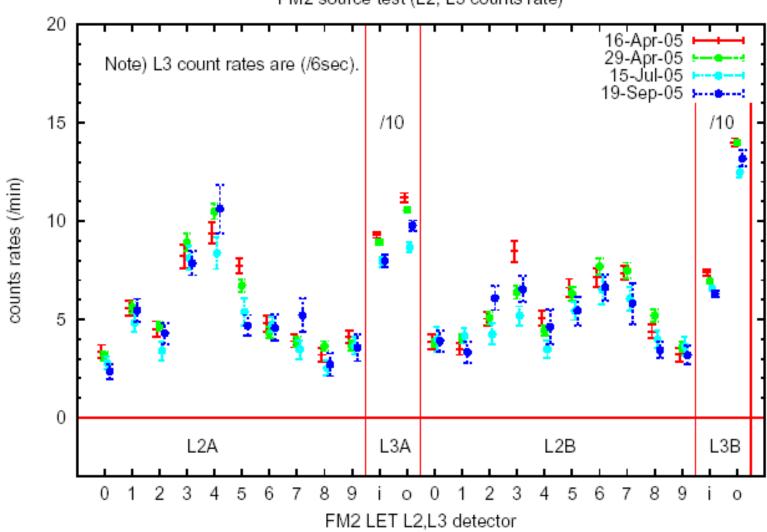
STEREO IMPACT

Sample FM2 LET leakage currents vs time during CPT



Sample FM 2 LET Trending Data (Source Tests)

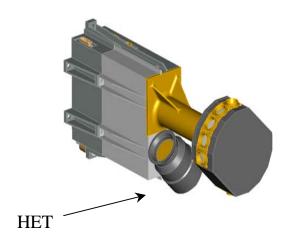
FM2 source test (L2, L3 counts rate)



HET Sensor Test Objectives

Subsystems

- Power supply (low voltage and bias)
- MISC processor/logic
- Serial communications with SEP Central
- PHASIC chips command registers
- Detector health
- Internal stimulus pulser
- Preamplifier voltage
- Housekeeping and Thermistors
- Software modes and processing checks



HET Subsystem Tests and Verification

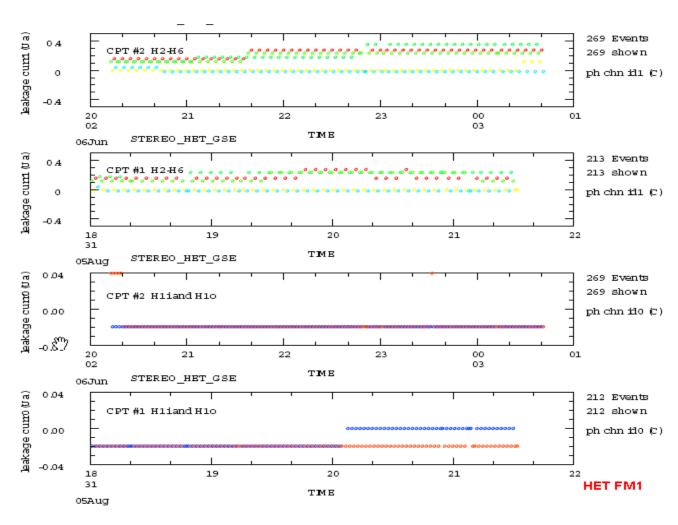
Subsystem Tests	Verification Parameters	FM1	FM2
Load binary into MISC memory *	Boot messages and checksums	Verified	Verified
Lookup table memory loads *	Table checksums and HK contents	Verified	Verified
Telemetry packet flow *	Science and HK contents	Verified	Verified
Command software modes *	Science and response packet contents	Verified	Verified
Command PHASIC chip registers (verifies interface with PHASIC chips)	PHASIC chips configured, HK and science packet contents	Verified	Verified
Detector leakage currents	HK contents	In range	In range
Noise level on detector channels	High (<100) and low gain (<20) rate counts	In range	In range

^{*} Verifies communications with SEP Central

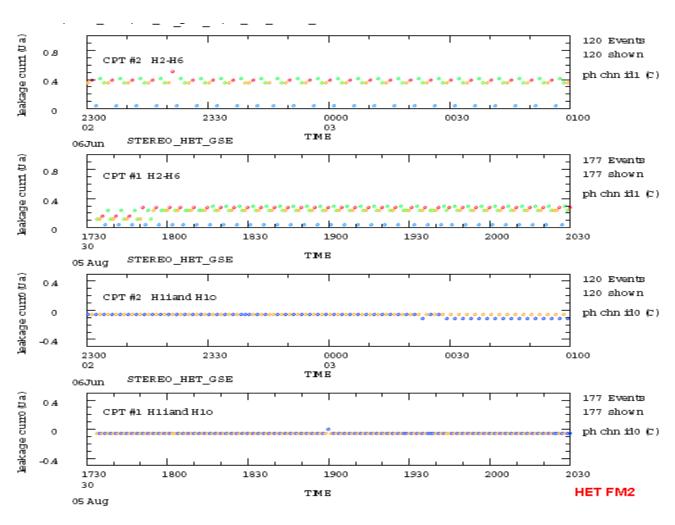
HET Subsystem Tests and Verification

Subsystem Tests	Verification Parameters	FM1	FM2
Internal stimulus pulser setup to pulse different event types	Stimulus events generated, read from PHASIC buffers, event types identified, queued and processed	Correct	Correct
Internal stimulus pulser setup to pulse channels with different offsets and gains	Offsets, gains and rate counts from science packets	In range	In range
Thermistors operation	Temperature values in HK packet	In range	In range
Software control of preamplifier voltage	Preamplifier voltage in HK packet	In range	In range
106 Ruthenium beta source placed over telescope (special test)	Events read from PHASIC buffers, types identified, queued and processed	In range	In range

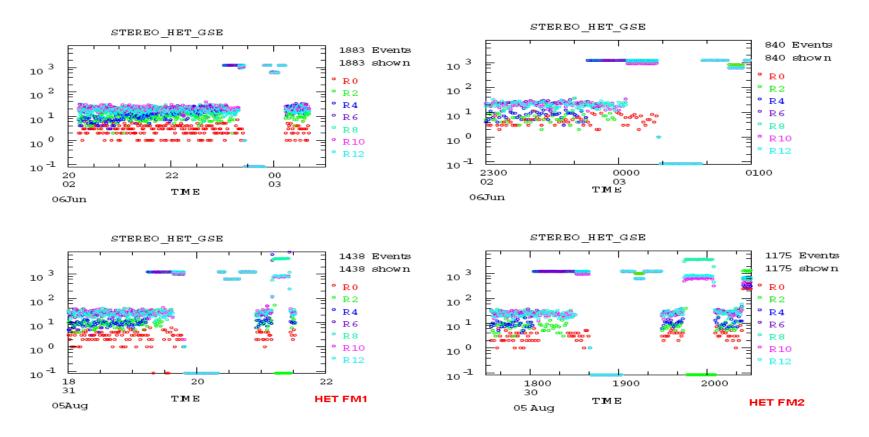
HET FM1 Detector Leakage Currents



HET FM2 Detector Leakage Currents



HET High Gain Singles Rates



* High counts periods during pulser test runs

Houskeeping Thermistors

HET Sensor Test Summary

Subsystems

_	Power supply (low voltage and bias)	OK
_	MISC processor/logic	OK
_	Serial communications with SEP Central	OK
_	PHASIC chips command registers	OK
_	Detector health	OK - Trended
_	Internal stimulus pulser	OK
_	Preamplifier voltage	OK - Trended

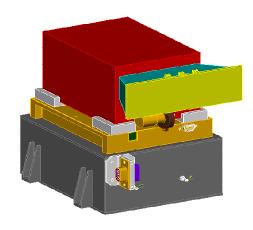
 Software modes and processing checks **Verified**

OK - Trended

SIT Sensor Test Objectives

Subsystems

- Power supply (low voltage, high voltage, bias)
- MISC processor/logic
- Serial communications with SEP Central
- Solid State Detector health
- Time of flight electronics
- Energy electronics
- Housekeeping and Thermistors
- Door actuator (not part of the normal CPT)
- Software modes and processing checks



SIT Subsystem Tests and Verification

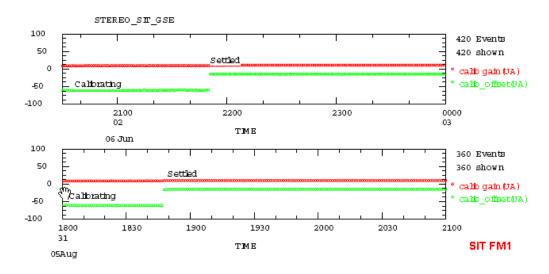
Subsystem Test	Verification Parameters	FM1	FM2
Load binary into MISC memory *	Boot messages and checksums	Verified	Verified
Lookup table memory loads *	Table checksums and HK contents	Verified	Verified
Telemetry packet flow *	Science and HK contents	Verified	Verified
Command software modes (command system) *	Science and response packet contents	Verified	Verified
External pulser (functionality test for the energy, tof and logic)	Stimulus events generated, queued and processed	Correct	Correct
HK and Thermistors (verifies low voltage and HK circuitry is working)	Temperature values in HK packet (Tof, Foil and SSD), ToF calibration offset and gain	In range	In range
High voltage enabled (verifies high voltage turn on)	Ramp to 26v (HV disable plug installed)	Ok	Ok
Noise level on detectors (monitors detector health)	Stop (0), Start (0), SSD (<20)	In range	In range

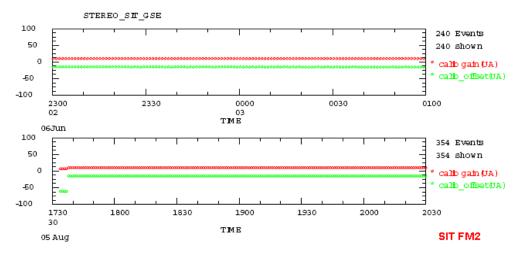
^{*} Verifies communications with SEP Central

SIT Subsystem Tests and Verification

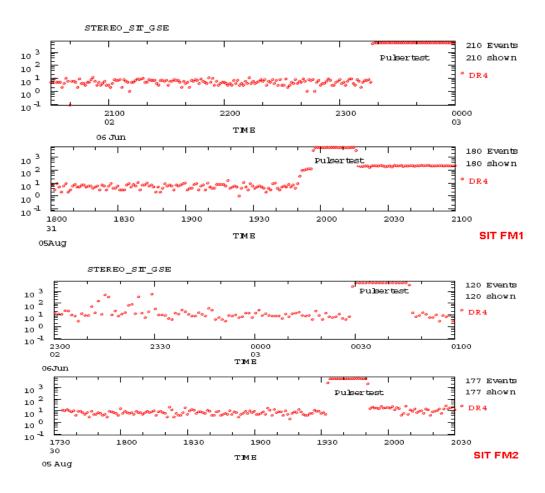
- Door actuator tests were successfully conducted on FM1 and FM2 during instrument TV and after spacecraft integration (not part of the normal CPT).
- Alpha tests were previously conducted on FM1 and FM2 under vacuum during TV (not part of the normal CPT). The Alpha tests perform a true end-to-end test of the SIT instrument, including high voltage at operating levels, all electronics and SSD and ToF detector systems.
- High voltage can only be turned up to operating voltages in high vaccum and with the disable plug removed.

SIT Tof Calibrations





SIT SSD Singles Rates



SIT Sensor Test Summary

Subsystems

Power supply (low voltage, high voltage, bias)MISC processor /logicOK

Serial communications with SEP Central

OK

Solid State Detector health
 Variable

Time of flight electronics
 OK - trended

Energy electronicsOK

Housekeeping and Thermistors
 OK - trended

Door actuator (not part of the normal CPT)

Software modes and processing checks